

## Virtual practicals & on-line tutorials

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**N°CP3: Clean Room Laboratory Practicals : IC device fabrication technologies**

Teachers:

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**Clean Room fabrication processes : Transistors, MOS capacitors, MIM ReRAM devices for a broad overview of basic processing steps and non destructive in situ and ex situ testings.**

These 2x1h45 covid-19 sessions of clean room practicals will provide an accurate and exhaustive description of each basic processing step (Cleaning, Doping, Lithography, Depositing and Etching) involved in the fabrication of IC devices. The clean room visit will be followed via our online guided tour. Technological key points will be addressed, along with up-to-date developments in the research lab, and all these issues will be discussed in view of potential industrialization.